


ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v1.8

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Title of Invention	Methods and apparatus for storing and manipulating diagonal interconnect lines of a multidimensional integrated circuit design.																																																								
<p>Application Number: 10/066387 *10/066387*</p> <p>Confirmation Number: 3740</p> <p>First Named Applicant: Tom Kronmiller</p> <p>Attorney Docket Number: SPLX.P0041</p> <p>Art Unit: 2171</p> <p>Search string: (5319743 or 5937408 or 6073134 or 6108657 or 6226647 or 6625611).pn.</p>																																																									
<p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"> <thead> <tr> <th>Init</th> <th>Cite.No.</th> <th>Patent No.</th> <th>Date</th> <th>Patentee</th> <th>Kind</th> <th>Class</th> <th>Subclass</th> </tr> </thead> <tbody> <tr> <td>TD</td> <td>1</td> <td>5319743</td> <td>1994-06-07</td> <td>Dutta</td> <td></td> <td></td> <td></td> </tr> <tr> <td>TD</td> <td>2</td> <td>5937408</td> <td>1999-08-22</td> <td>Shoup</td> <td></td> <td></td> <td></td> </tr> <tr> <td>TD</td> <td>3</td> <td>6073134</td> <td>2000-06-06</td> <td>Shoup</td> <td></td> <td></td> <td></td> </tr> <tr> <td>TD</td> <td>4</td> <td>6108657</td> <td>2000-08-10</td> <td>Shoup</td> <td></td> <td></td> <td></td> </tr> <tr> <td>TD</td> <td>5</td> <td>6226647</td> <td>2001-05-01</td> <td>Venkatasubramanian</td> <td></td> <td></td> <td></td> </tr> <tr> <td>TD</td> <td>6</td> <td>6625611</td> <td>2003-09-23</td> <td>Teig</td> <td></td> <td></td> <td></td> </tr> </tbody> </table>		Init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	TD	1	5319743	1994-06-07	Dutta				TD	2	5937408	1999-08-22	Shoup				TD	3	6073134	2000-06-06	Shoup				TD	4	6108657	2000-08-10	Shoup				TD	5	6226647	2001-05-01	Venkatasubramanian				TD	6	6625611	2003-09-23	Teig			
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